			UNITED STATES DEPARTM United States Patent and T Address: COMMISSIONER OF P. Washington, D.C. 20231 www.uspto.gov	rademark Office ATENTS AND TRADEMAR
APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/591,682	06/09/2000	Sheila M. Rader	CS-10246	3605
7590 03/06/2003 Motorola inc Personal Communications Sector Intellectual Property Department (MCS) 600 North US Highway 45 AN475 Libertyville, IL 60048			EXAMINER	
			THAI, XUAN MARIAN	
			ART UNIT	PAPER NUMBER
,			2181	
			DATE MAILED: 03/06/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

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<u> </u>	_	Application No.	Applicant(s)
آھ. 1		09/591,682	RADER ET AL.
	Office Action Summary	Examiner	Art Unit
		XUAN M. THAI	2181
Peridfo	- The MAILING DATE f this community r Reply	cation appears n the c ver sheet	with the correspondenc address
THE N - Exten after S - If the - If NO - Failur - Any re	CRTENED STATUTORY PERIOD FO MAILING DATE OF THIS COMMUNIC sions of time may be available under the provisions of SIX (6) MONTHS from the mailing date of this commu- period for reply specified above is less than thirty (30 period for reply is specified above, the maximum stat e to reply within the set or extended period for reply v aply received by the Office later than three months af d patent term adjustment. See 37 CFR 1.704(b).	CATION. of 37 CFR 1.136(a). In no event, however, may unication.) days, a reply within the statutory minimum of 1 tutory period will apply and will expire SIX (6) M will by statute, cause the application to become	a reply be timely filed hirty (30) days will be considered timely. ONTHS from the mailing date of this communication. ABANDONED (35 U.S.C. § 133).
1)🖂	Responsive to communication(s) file	ed on <u>09 J<i>une</i> 2000</u> .	
2a)	This action is FINAL . 2	b) This action is non-final.	
3) <u></u> Dispositie	Since this application is in condition closed in accordance with the practi on of Claims	for allowance except for formal n ice under <i>Ex parte Quayle</i> , 1935 (natters, prosecution as to the merits is C.D. 11, 453 O.G. 213.
4)⊠	Claim(s) <u>1-37</u> is/are pending in the a	ipplication.	
4	4a) Of the above claim(s) is/ar	e withdrawn from consideration.	
5)	Claim(s) is/are allowed.		
6)	Claim(s) is/are rejected.		
7)	Claim(s) is/are objected to.		
-	Claim(s) <u>1-37</u> are subject to restrictio on Papers	on and/or election requirement.	
	The specification is objected to by the	Examiner.	
,	The drawing(s) filed on is/are:		y the Examiner.
,	Applicant may not request that any obje		
11) 🗌 1	The proposed drawing correction filed	on is: a) approved b)	disapproved by the Examiner.
	If approved, corrected drawings are req	uired in reply to this Office action.	
12) 🗌 1	The oath or declaration is objected to	by the Examiner.	
riority u	nder 35 U.S.C. §§ 119 and 120		
13)	Acknowledgment is made of a claim	for foreign priority under 35 U.S.C	C. § 119(a)-(d) or (f).
a)[All b) Some * c) None of:		
	1. Certified copies of the priority of	documents have been received.	
	2. Certified copies of the priority of	documents have been received ir	Application No
		ational Bureau (PCT Rule 17.2(a)	
14) 🗌 A	cknowledgment is made of a claim fo	or domestic priority under 35 U.S.	C. § 119(e) (to a provisional application
	The translation of the foreign lan		
ttachment	•		
) Notice () Notice () Notice ()	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (P nation Disclosure Statement(s) (PTO-1449) Pa	TO-948) 5) 🔲 Notice	w Summary (PTO-413) Paper No(s) of Informal Patent Application (PTO-152)
Patent and Tr O-326 (Re	ademark Office	Office Action Summary	Part of Paper No. 5

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Application/Control Number: 09/591,682 Art Unit: 2181

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DETAILED ACTION

Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-16 and 29-34, drawn to direct memory access system and methods, classified in class 710, subclass 22.
- II. Claims 17-22, drawn to a memory system, which includes the particulars of multiple bus architecture, classified in class 710, subclass 307.
- III. Claims 23-28 and 35-37, drawn to method and system for memory refresh, classified in class 711, subclass 106.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions II and I are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed because the memory access system and method comprises a programmable scheduler for prioritizing data transfers over the programmable direct memory access data channels are distinct from the subcombination of a multiplexer coupling a plurality of buses having different widths and using a memory as buffer and does not requires the particulars of the subcombination as claimed. The subcombination has separate utility such as in general data transfer using the claimed bus architecture.

Application/Control Number: 09/591,682 Art Unit: 2181

3. Inventions III and II are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed does not require the particulars of the subcombination as claimed because "the method of memory refresh comprising periodically refreshing a portion of the dynamic random access memory less than its predetermined capacity and additionally comprising adjusting a refresh rate of the dynamic random access memory based on ambient temperature to reduce power consumed as the memory portion is refreshed" not required in the combination for patentability. The subcombination has separate utility such as in general data transfer using the claimed bus architecture.

An attempt to contact the attorney of record, Mr. Michael Soldner, Reg. No. 41,455 at
(847) 523-2585 on March 5, 2003 but did not result in an election.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to XUAN M. THAI whose telephone number is 703-308-2064. The examiner can normally be reached on Flexible Work Schedule.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 703-305-4815. The fax phone numbers for the organization where this application or proceeding is assigned are 703-746-7239 for regular communications and 703-746-7238 for After Final communications.

Application/Control Number: 09/591,682 Art Unit: 2181

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

XUAN M. THAI Primary Examiner Art Unit 2181

XMT March 5, 2003

14